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(use as many sheets as necessary)				Filing Date	herewith
Sheet	1	of	1	First Named Inventor	Alter, M
				Group Art Unit	
				Examiner Name	
				Attorney Docket Number	M077PI

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher city and/or country where published	T ²
<i>PF</i>	1	Dr. Luu Nguyen et al, Assembly Considerations for micro SMD Wafer-Level CSPs, Chip Scale Review, May/June 2000, page 48 et seq.	
<i>PF</i>	2	James L. Young, Wafer-Level Processing: Working Smarter, Chip Scale Review, May 1997, p. 28 et seq.	
<i>PF</i>	3	Dr. Philip Garrou, Wafer-Level Packaging Has Arrived, Semiconductor International, October 2000, p. 119 et seq.	

Examiner Signature	<i>Ross Miller</i>	Date Considered	1/23/05
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